

## UNITED STAT: DEPARTMENT OF COMMERCE Patent and Tracemark Office

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Γ	SERIAL NUMBER	FIUNG DATE	FIR	ST NAMED INVENTOR		ATTORNEY DOCKET NO.
-	07/653,017	02/08/91	LIDOW		А	IR-1013 (CONT
	··· ·					EXAMINER
					LOKE,S	
	OSTROLENK,				ART UNIT	PAPER NUMBER
	GERB & SOFFEN 1180 AVENUE OF THE AP NEW YORK, NY 10036-8			258	5	
					DATE MAILED:	05/24/91
T	his is a communication from COMMISSIONER OF PATEN	the examiner in charge ITS AND TRADEMARKS	of your application.			

7 This s	pplication has been examined Responsive to communication filed on	This action is made final.					
	described and portion for response to this action is set to expire 5 month(s), days for						
enociarie allure to r	espond within the period for response will cause the application to become abandoned. 35 U.S.C. 133	•					
art i 🚮	HE FOLLOWING ATTACHMENT(S) ARE PART OF THIS ACTION:						
1. 2	NOTICE OF ACT CITED BY Applicable, 1 To 1 To 1	p, PTO-948. t Application, Form PTO-152					
en ( <u>[</u>	SUMMARY OF ACTION						
1 🔀	Claims 12 - 19	are pending in the application					
	Of the above, claims	are withdrawn from consideration.					
2	Claims	have been cancelled.					
	Claims						
4. 🛛	Claims						
5. 🗀	Ctaims	are objected to.					
6.	Claims are subject to rest						
7.	This epplication has been filed with informal drawings under 37 C.F.R. 1.85 which are acceptable for examination purposes.						
8. 🗀	Formal drawings are required in response to this Office ection.						
9.	The corrected or substitute drawings have been received on U are ecceptable; not acceptable (see explanation or Notice re Patent Drawing, PTO-948).	nder 37 C.F.R. 1.84 these drawing					
10.	The proposed additional or substitute sheet(s) of drawings, filed on has (have) be examiner;    disapproved by the examiner (see explanation).	en approved by the					
11.	The proposed drawing correction, filed, has been approved; disappro						
12.	Acknowledgement is made of the claim for priority under U.S.C. 119. The certified copy has been been filed in parent application, serial no; filed on;	received not been received					
13.	Since this application apppears to be in condition for allowance except for formal matters, prosecution accordance with the practice under Ex parte Quayle, 1935 C.D. 11; 453 O.G. 213.	as to the merits is closed in					
14.	Other*						

EXAMINER'S ACTION

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Claims 12-19 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-14 of U.S. Patent No. 5,008,725 in view of Hanes et al.

The obviousness-type double patenting rejection is a judicially established doctrine based upon public policy and is primarily intended to prevent prolongation of the patent term by prohibiting claims in a second patent not patentably distinct from claims in a first patent. In re Vogel, 164 USPQ 619 (CCPA 1970). A timely filed terminal disclaimer in compliance with 37 C.F.R. § 1.321(b) would overcome an actual or provisional rejection on this ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 C.F.R. § 1.78(d).

The following is a quotation of 35 U.S.C. § 103 which forms the basis for all obviousness rejections set forth in this Office action:

A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Subject matter developed by another person, which qualifies as prior art only under subsection (f) or (g) of section 102 of this title, shall not preclude patentability under this section where the subject matter and the claimed invention were, at the time the invention was made, owned by the same person or subject to an obligation of assignment to the same person.

Claims 12-19 are rejected under 35 U.S.C. § 103 as being unpatentable over Lidow et al (5,008,725) in view of Hanes et al.

Lidow et al claim a high power MOSFET in fig.5. It comprises:

(a) an type layer [21] formed on a wafer;

- (b) a plurality of hexagonal base regions [22,23] formed in the n type layer [21];
- (c) each of base regions [22,23] is surrounded by a symmetric hexagonal lattice;
- (d) each side of the base regions [22,23] parallel to an adjacent side of another base regions;
- (e) source regions [26,27] formed in the base regions [22,23] and channel regions [60,61,62,63] formed between the source and base regions;
- (f) a source electrode [50] connected to the source and base regions [26,27,22,23] and a drain electrode [51] connected to a wafer;
- (g) insulated gate electrodes [40,41,42] formed on the channel regions [60,61,62,63].

Hanes et al discloses a power MOSFET in fig.8 and 16. It comprises:

- (a) a n type base region [22] formed on a n type wafer [24];
- (b) a plurality of n type source regions [73] formed on a plurality of p type base regions [71] which formed on the n type base region [22];
- (c) insulated gate electrode [92] formed on the channel regions between the source and base regions [73,22];
- (d) source and drain electrodes [92, 18] formed on the source and

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drain regions [73,24] respectively;

(e) a gate pad electrode [50] formed on the device and gate fingers  $[51/-51_{n+1},61/-61_{n+1}]$  extending from the gate electrode [50].

Since both Lidow et al and Hanes et al teach an insulated gate high power MOSFET, it would have been obvious to one of ordinary skills in the art to have the gate electrode and fingers of Hanes et al in Lidow et al because it is common insulated gate structure and it reduces the R-C delay constant of the device.

Any inquiry concerning this communication should be directed to Loke at telephone number (703) 308-3700.

Loke/dm May 21, 1991

ROLF HILLE
SUPERVISORY PATENT EXAMINER
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